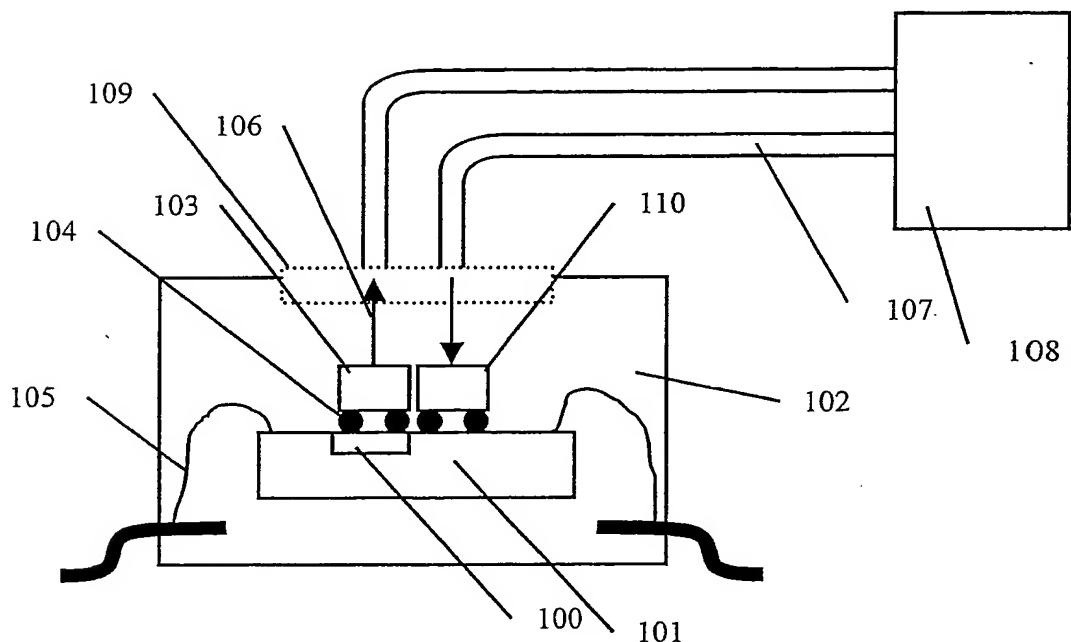


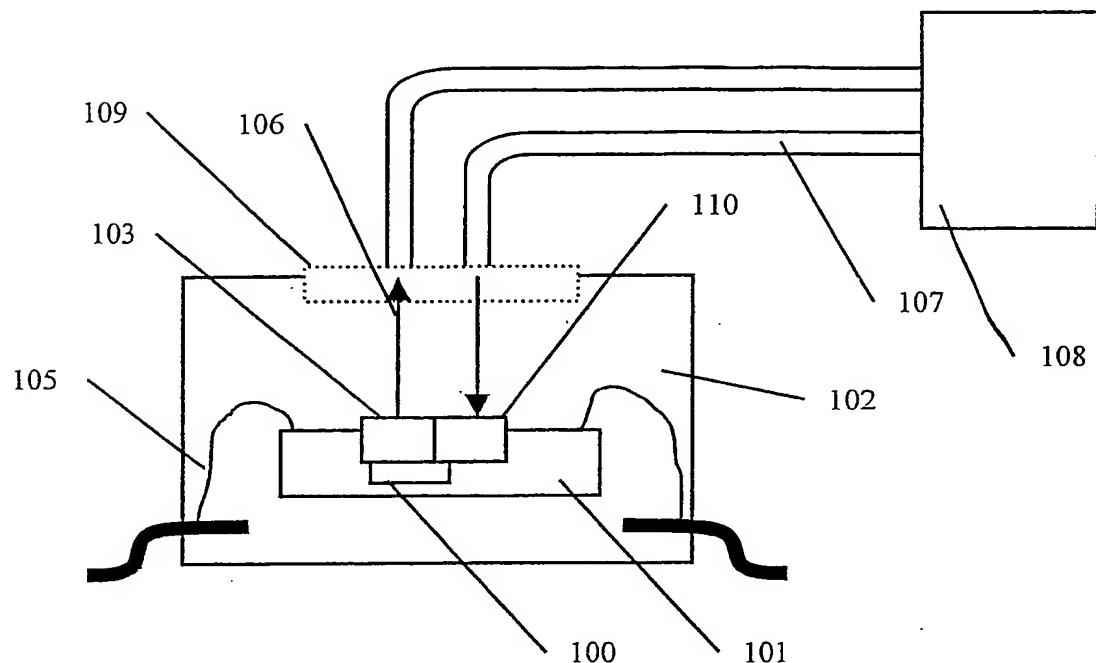
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1st Named Inventor: Klaus Dieter McDonald-Maier
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Figure 1



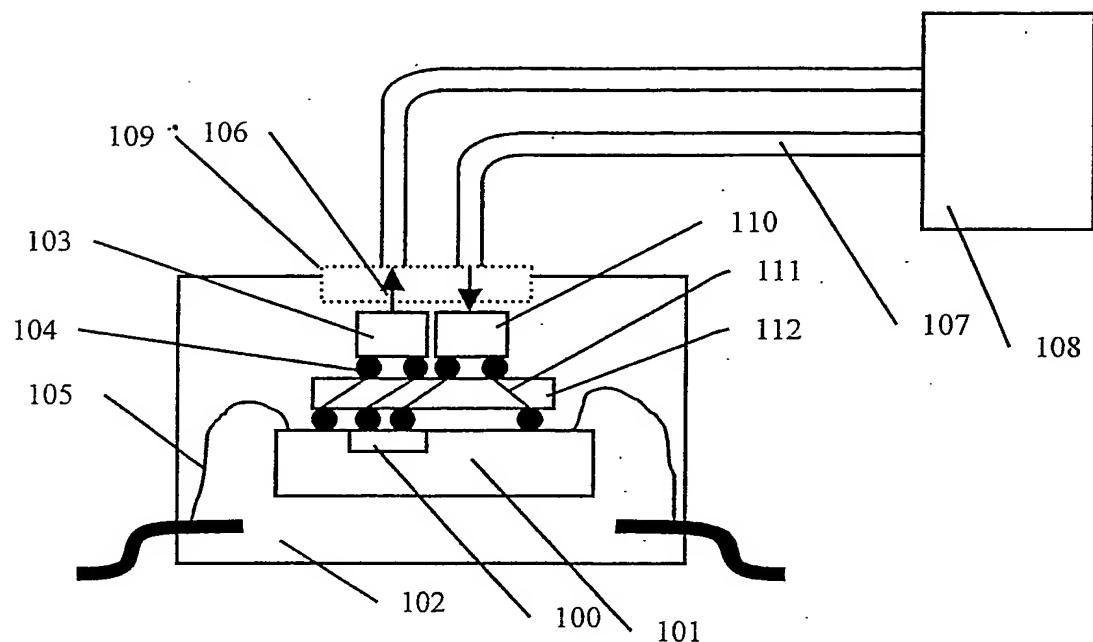
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Figure 2

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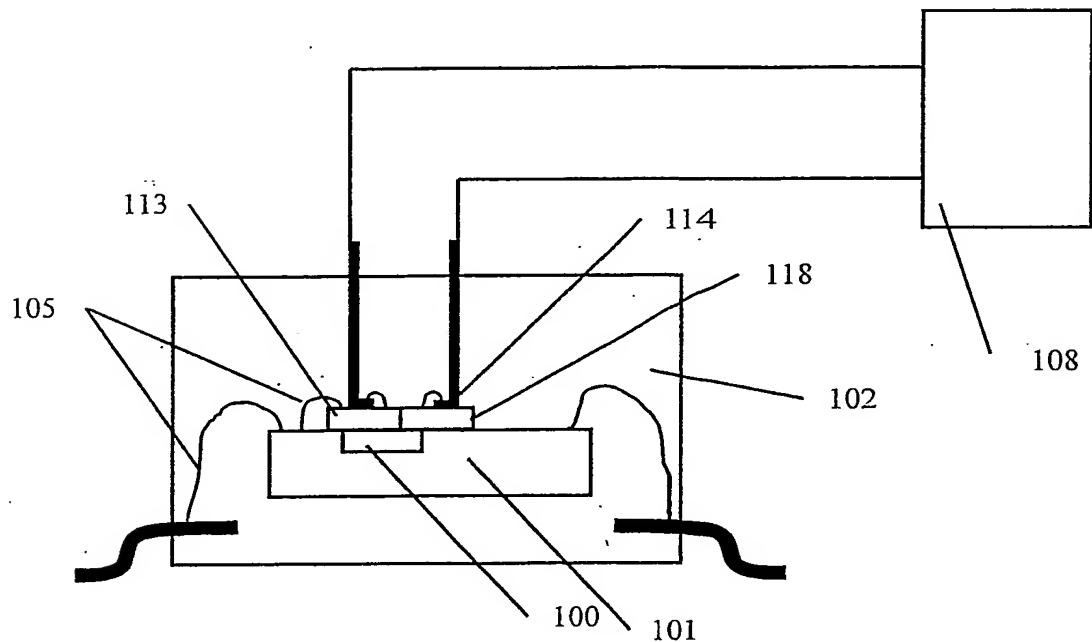
Figure 3



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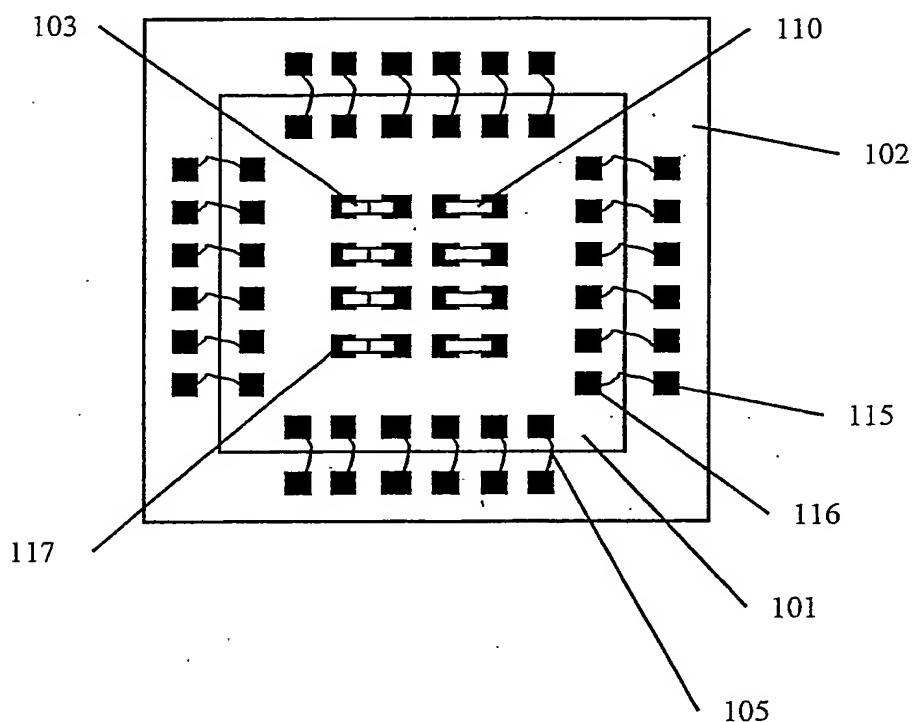
Figure 4



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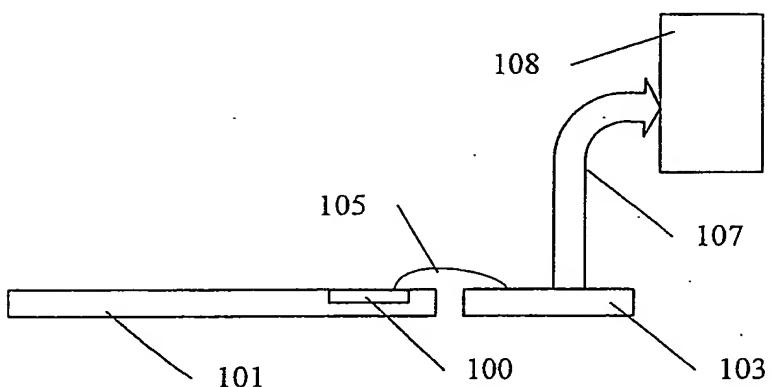
Figure 5



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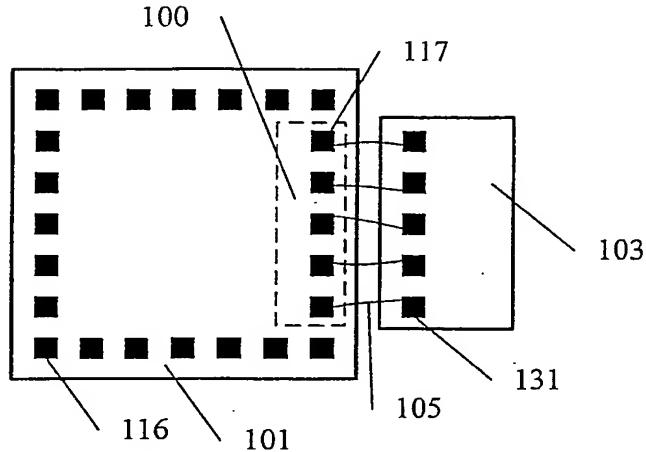
6/8

Figure 6



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Figure 7

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Figure 8

